

1-Line , Bi-directional , Transient Voltage Supperssor

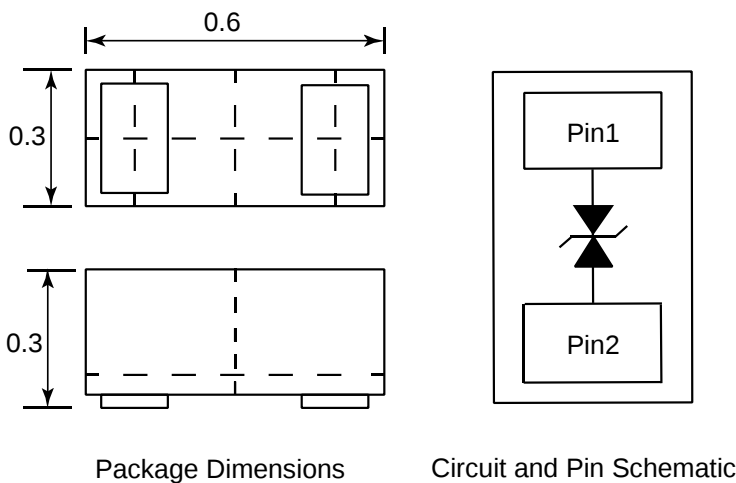
Description

PESDU0511P0 is a bi-directional TVS (Transient Voltage Suppressor). It has been specifically designed to protect sensitive electronic components which are connected to low speed data lines and control lines from over-stress caused by ESD (Electrostatic Discharge) and Lightning . PESDU0511P0 may be used to provide ESD protection up to $\pm 30\text{kV}$ (air and contact discharge) according to IEC61000-4-2, and withstand peak pulse current up to $7\text{A}(8/20\mu\text{s})$ according to IEC61000-4-5 . PESDU0511P0 is available in DFN0603-2 package. Standard products are Pb-free and Halogen-free.

Features

- Operating voltage: 5V
- Low clamping voltage
- 2-pin leadless package
- Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: $\pm 30\text{kV}$
 - Contact discharge: $\pm 30\text{kV}$
 - IEC61000-4-5 (Lightning) $7\text{A}(8/20\mu\text{s})$
- RoHS Compliant

Dimensions and Pin Configuration



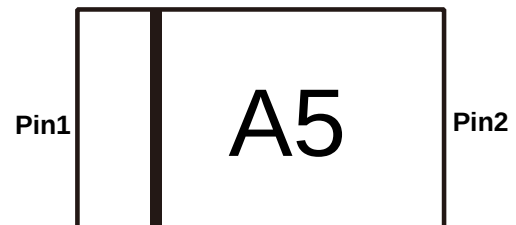
Mechanical Characteristics

- Package: DFN0603-2 (0.6×0.3×0.3mm)
- Case Material: “Green” Molding Compound.
- Moisture Sensitivity: Level 1 per J-STD-020
- Marking Information: See Below

Applications

- Cellular Handsets
- USB V_{BUS} and CC Line Protection
- Microphone Line Protection
- GPIO Protection

Marking Information



A5 = Device Marking Code

Ordering Information

Part Number	Shipping	Reel Size
PESDU0511P0	10000/Tape & Reel	7 inch

Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 μs)	P_{PK}	84	W
Peak Pulse Current (8/20 μs)	I_{PP}	7	A
ESD per IEC 61000-4-2 (Air)	V_{ESD}	± 30	kV
ESD per IEC 61000-4-2 (Contact)		± 30	
Lead temperature	T_L	260	$^{\circ}\text{C}$
Junction Temperature	T_J	125	$^{\circ}\text{C}$
Operating Temperature Range	T_{OP}	$-40 \sim +125$	$^{\circ}\text{C}$
Storage Temperature Range	T_{STG}	$-55 \sim +150$	$^{\circ}\text{C}$

Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition
Reverse Working Voltage	V_{RWM}			5	V	
Breakdown Voltage	V_{BR}	5.3			V	$I_T = 1\text{mA}$
Reverse Leakage Current	I_R			0.1	μA	$V_{RWM} = 5\text{V}$
Clamping voltage ¹⁾	V_{CL}		9		V	$I_{PP} = 16\text{A}$, $t_p = 100\text{ns}$
Dynamic resistance ¹⁾	R_{DYN}		0.2		Ω	
Clamping voltage ²⁾	V_{CL}		9		V	$V_{ESD} = 8\text{kV}$
Clamping Voltage ³⁾	V_C		7	8	V	$I_{PP} = 1\text{A}$ (8/20 μs pulse)
Clamping Voltage ³⁾	V_C		10	12	V	$I_{PP} = 7\text{A}$ (8/20 μs pulse)
Junction Capacitance	C_J		16	20	pF	$V_R = 0\text{V}$, $f = 1\text{MHz}$

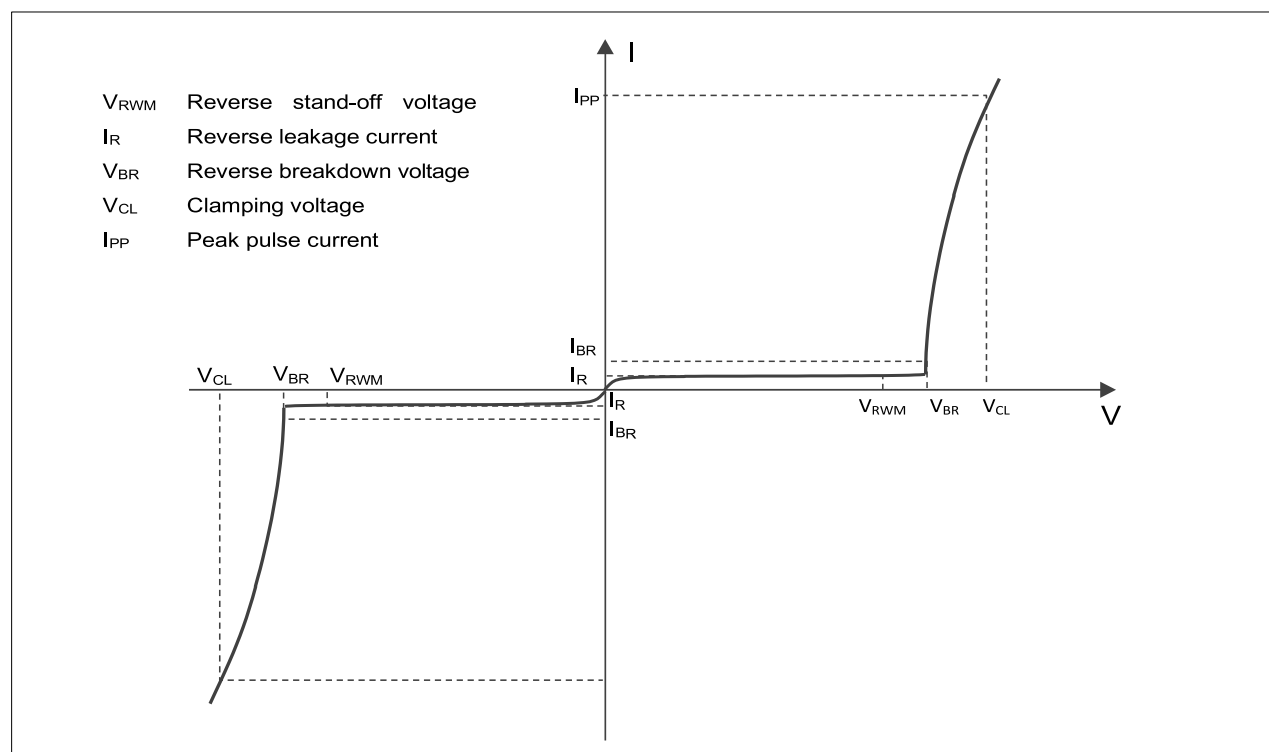
Notes:

1) TLP parameter: $Z_0 = 50\Omega$, $t_p = 100\text{ns}$, $t_r = 2\text{ns}$, averaging window from 60ns to 80ns. R_{DYN} is calculated from 4A to 16A.

2) Contact discharge mode, according to IEC61000-4-2.

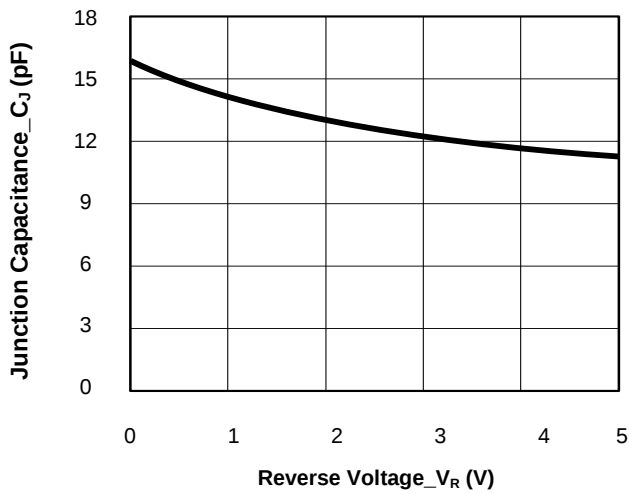
3) Non-repetitive current pulse, according to IEC61000-4-5.

Electrical characteristics ($T_A = 25^\circ\text{C}$, unless otherwise noted)

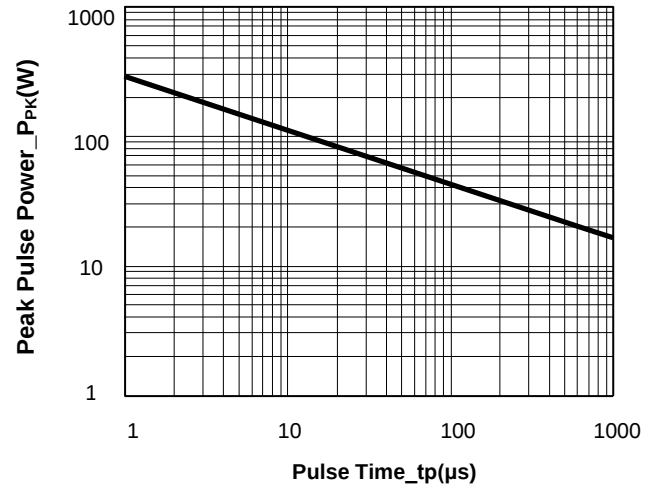


Definitions of electrical characteristics

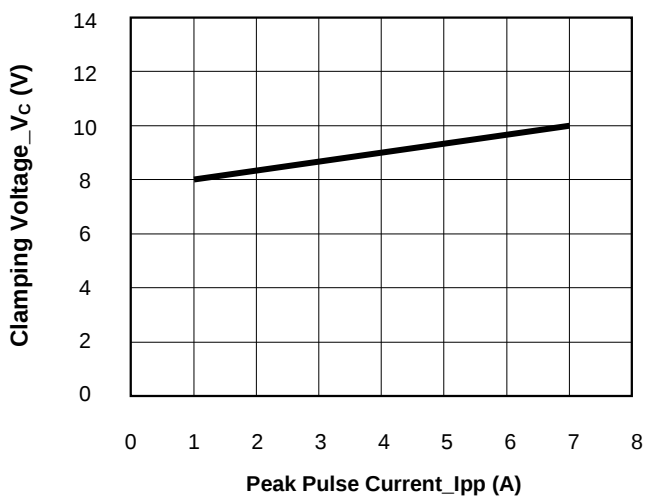
Typical Performance Characteristics ($T_A=25^\circ\text{C}$ unless otherwise Specified)



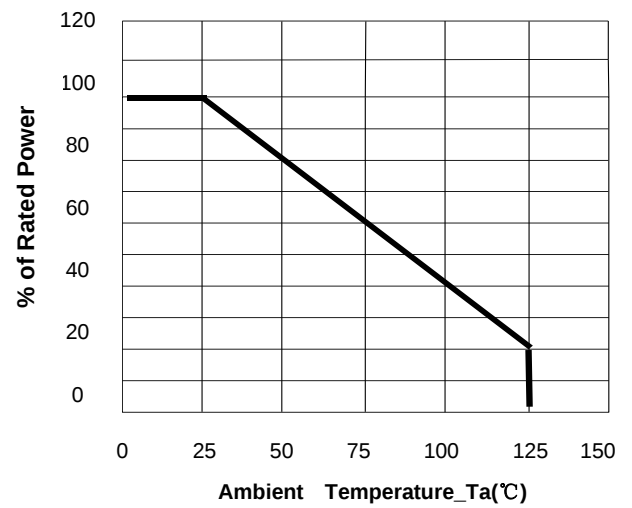
Junction Capacitance vs. Reverse Voltage



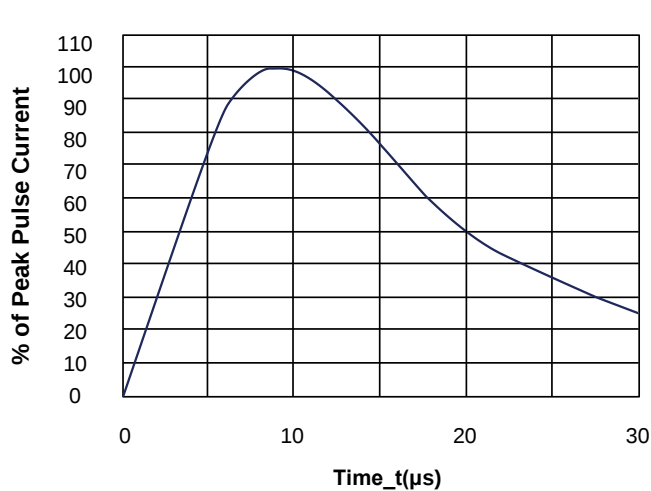
Peak Pulse Power vs. Pulse Time



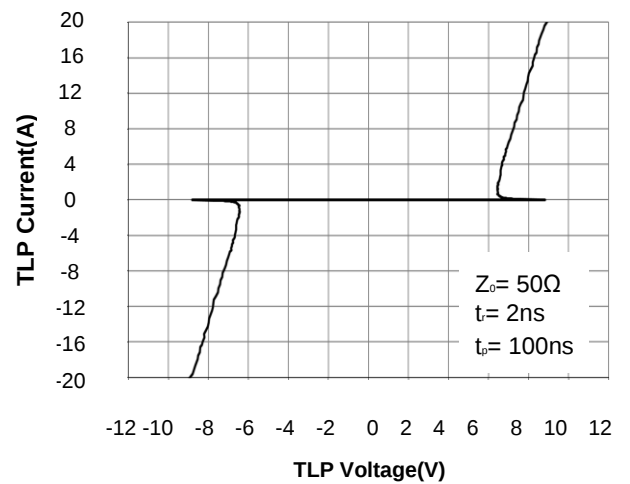
Clamping Voltage vs. Peak Pulse Current



Power Derating Curve

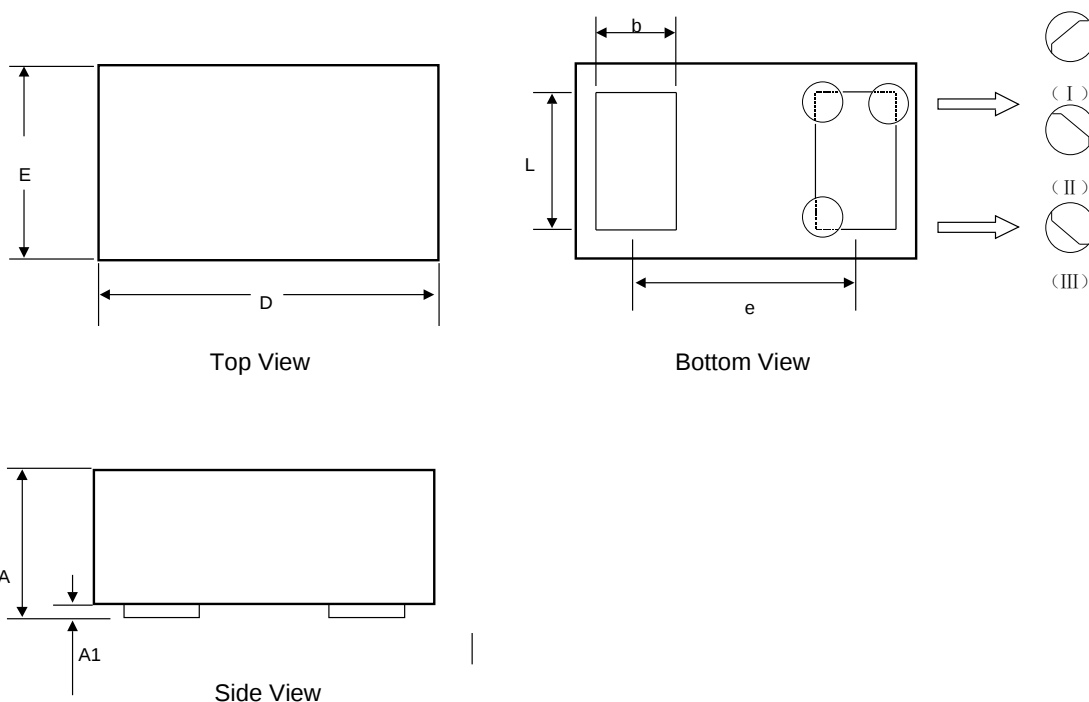


8/20 μs Pulse Waveform



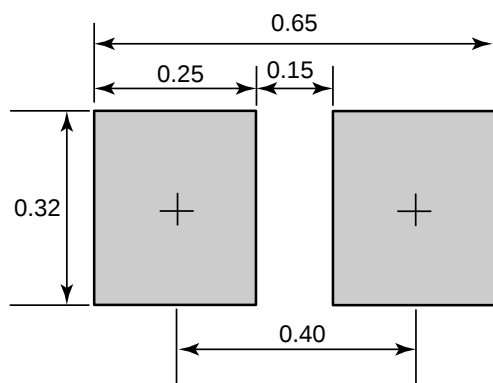
TLP Measurement

DFN0603-2 Package Outline Drawing



Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
A	0.230	0.300	0.350
A1	0.000	-	0.050
D	0.550	0.600	0.670
E	0.250	0.300	0.370
b	0.160	0.190	0.230
L	0.215	0.245	0.275
e	0.360 BSC		

Recommended PCB Layout (Unit: mm)

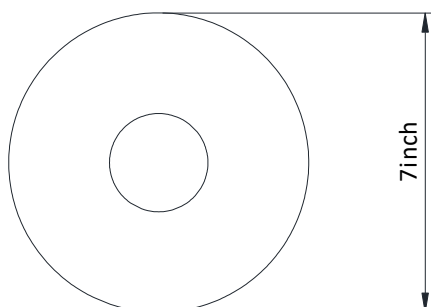


Notes:

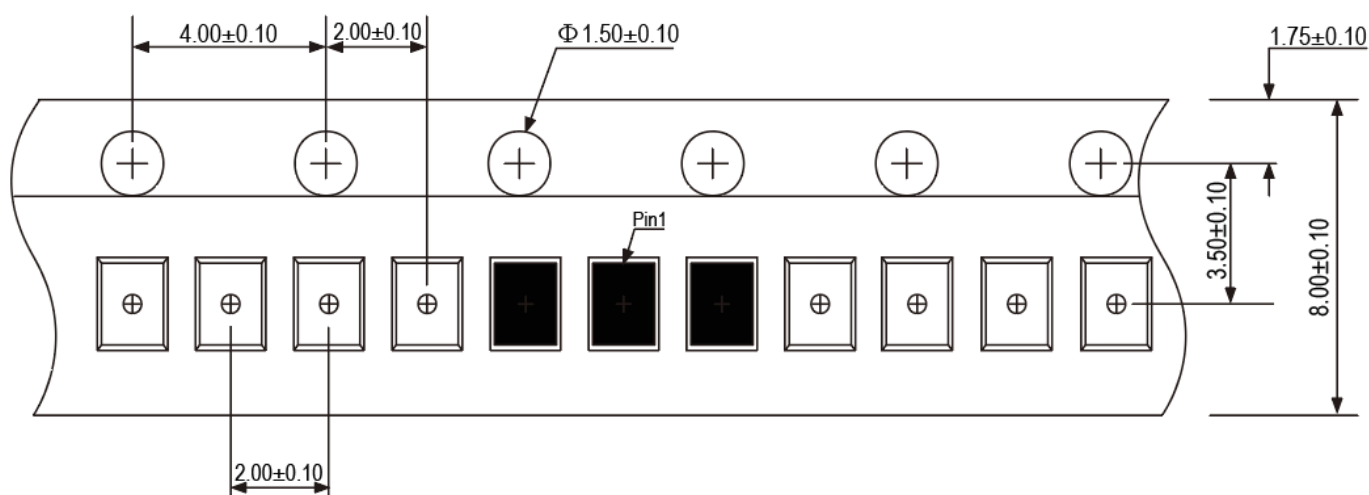
This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.

TAPE AND REEL INFORMATION

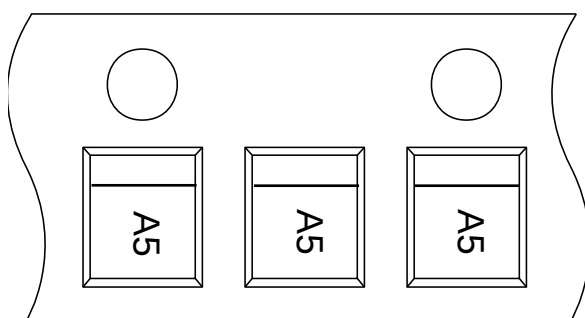
Reel Dimensions



Tape Dimensions



Unit:mm



→
User Direction of Feed

IMPORTANT NOTICE

The information given in this document is believed to be accurate and reliable but shall in no event be regarded as a guarantee of conditions or characteristics. PN-Silicon assumes no responsibility for any errors in this document, or for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of PN-Silicon.

The product listed in this document are designed to be used with ordinary electronic equipment or devices and are not authorized to used with equipment or devices which require an extremely high level of reliability and the malfunction of with would directly endanger human life (such as medical instruments, aerospace machinery, nuclear-reactor controllers, automotive and other safety device.)

The  logo is a registered trademark of PN-Silicon co., ltd which reserves the right to make changes to the product or this document at any time without notice. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. PN-Silicon makes no warranty, representation or guarantee, express or implied, regarding the suitability of its products for any particular purpose. All rights reserved.